

FIG.1 (PRIOR ART)

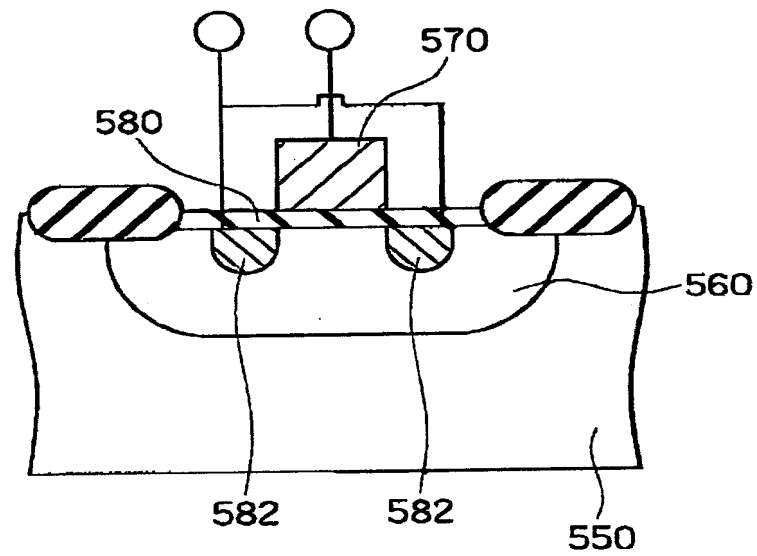


FIG.2 (PRIOR ART)

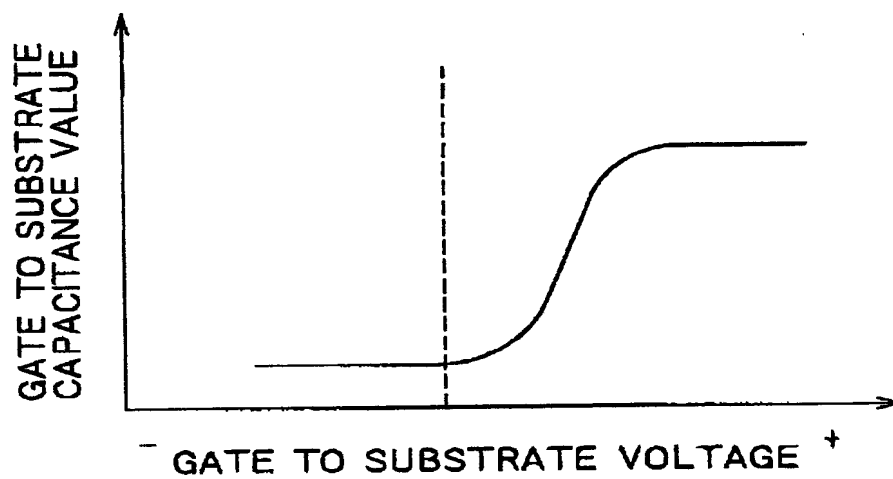


FIG.3 (PRIOR ART)

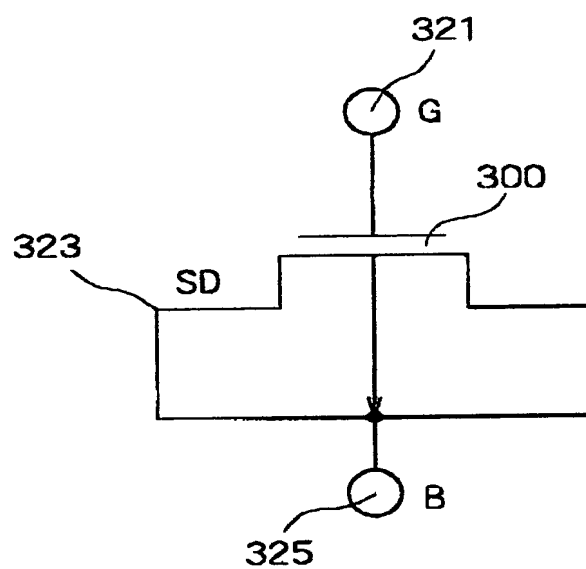
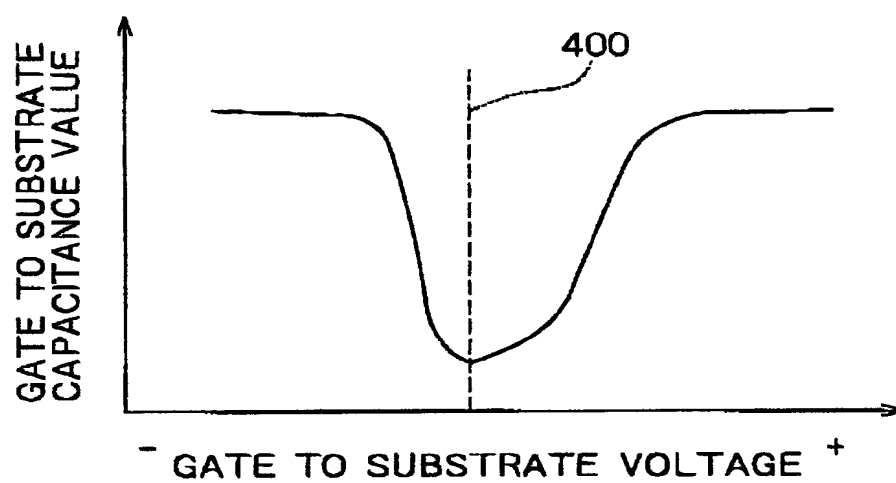


FIG. 4 (PRIOR ART)



A graph showing the relationship between Gate to Substrate Capacitance Value (Y-axis) and Gate to Substrate Voltage (X-axis). The X-axis is labeled with a minus sign (-) on the left and a plus sign (+) on the right. The curve shows a minimum capacitance value in the middle, flanked by two regions of higher capacitance. Key points on the curve are labeled: 610 (left high capacitance region), 400 (minimum capacitance point), 600 (right high capacitance region), and 660 (further right high capacitance region). A horizontal line with arrows at both ends is drawn across the graph, and a dashed line is shown on the right side.

FIG. 7

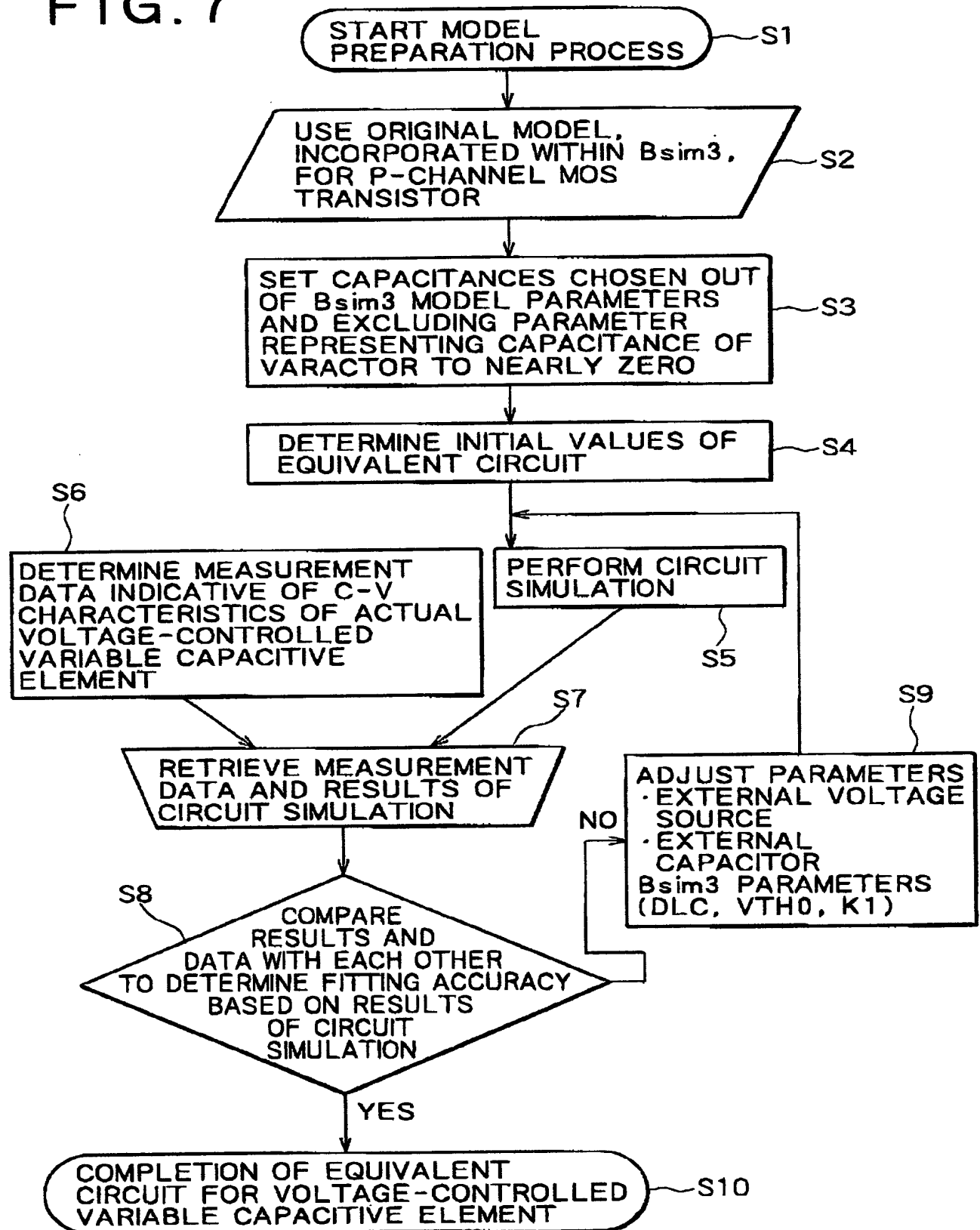


FIG. 8

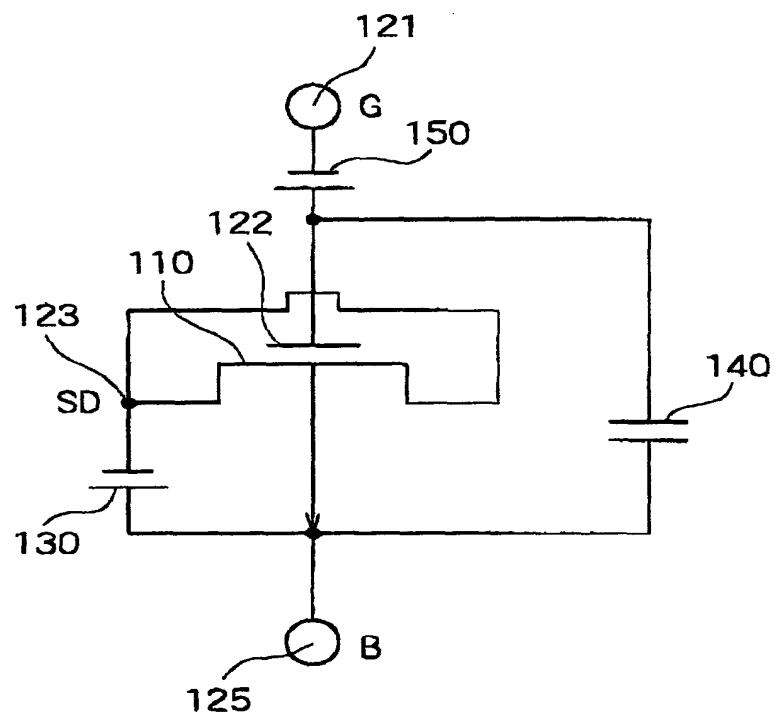


FIG. 9

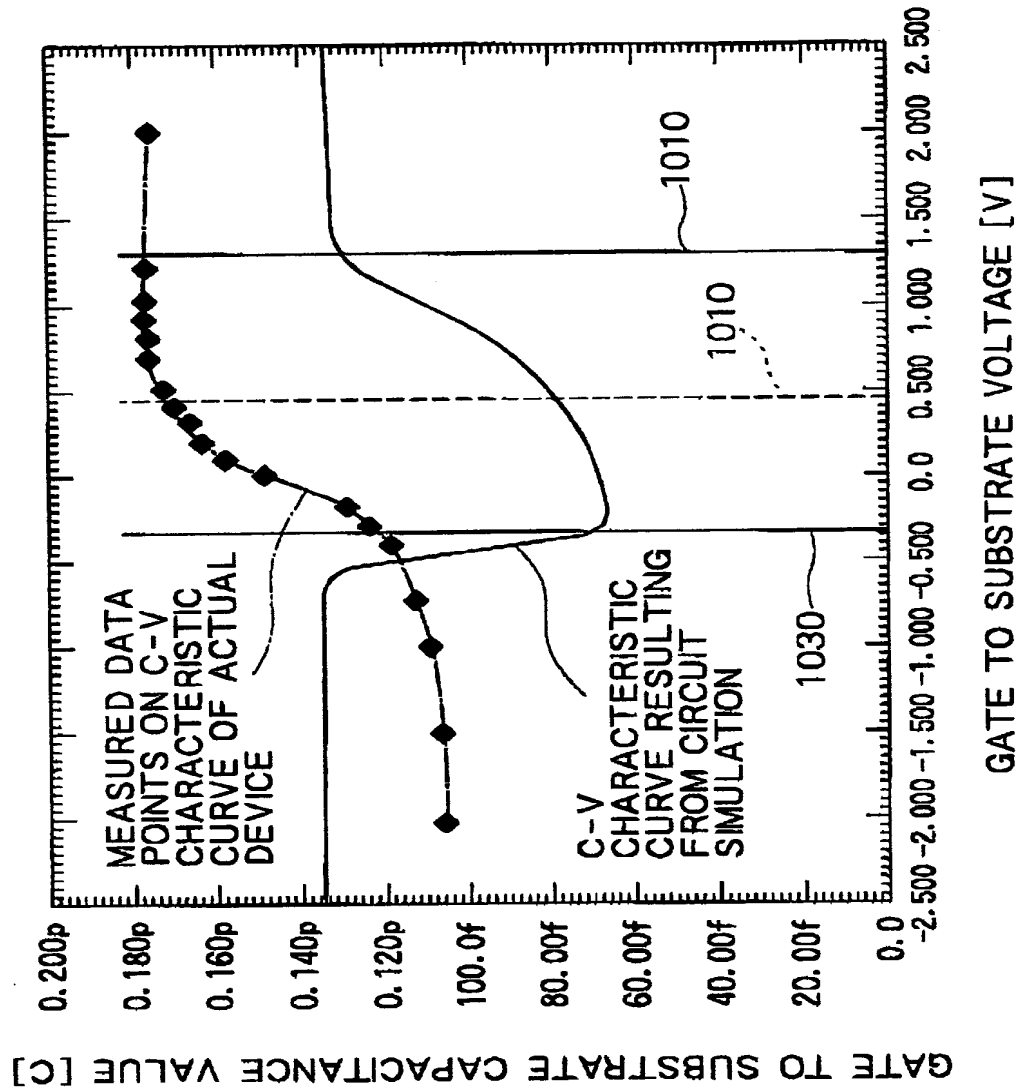


FIG. 10

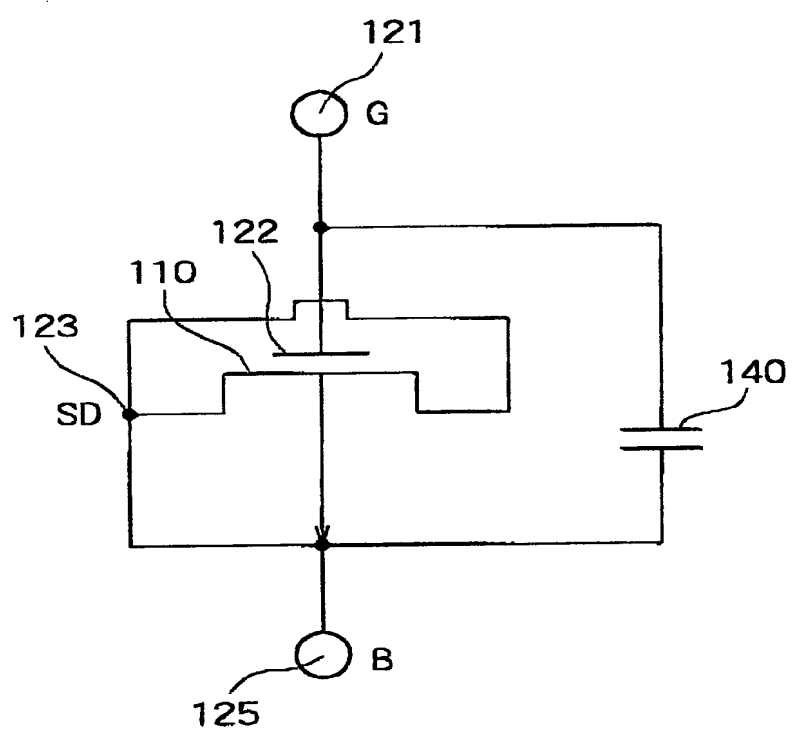


FIG. 11

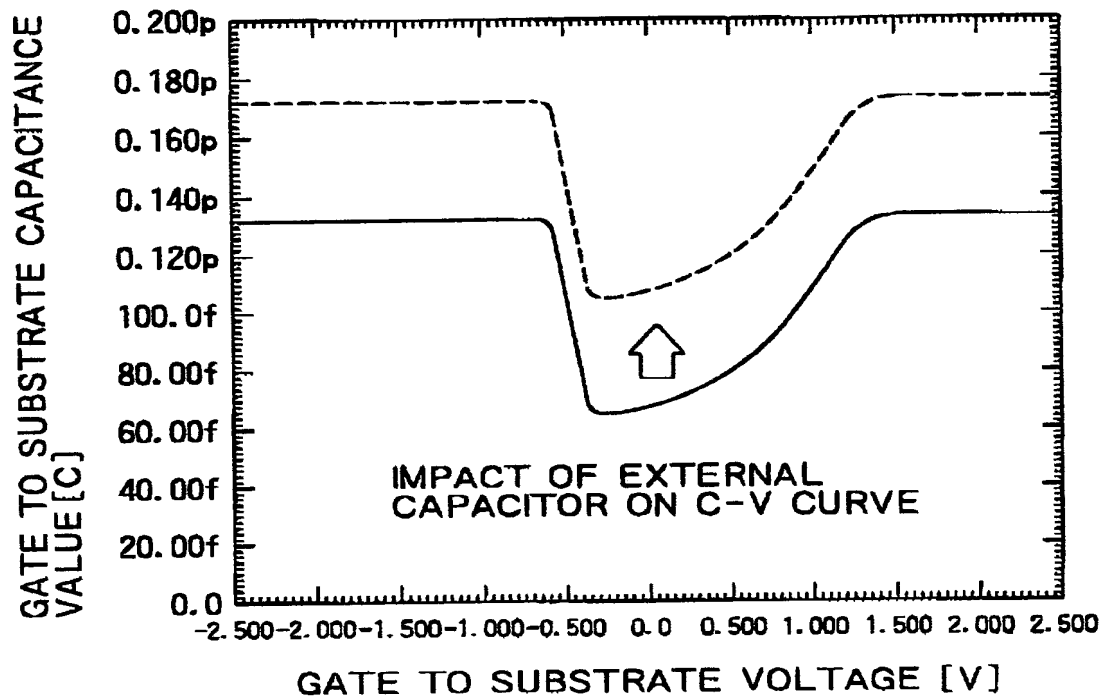


FIG. 12

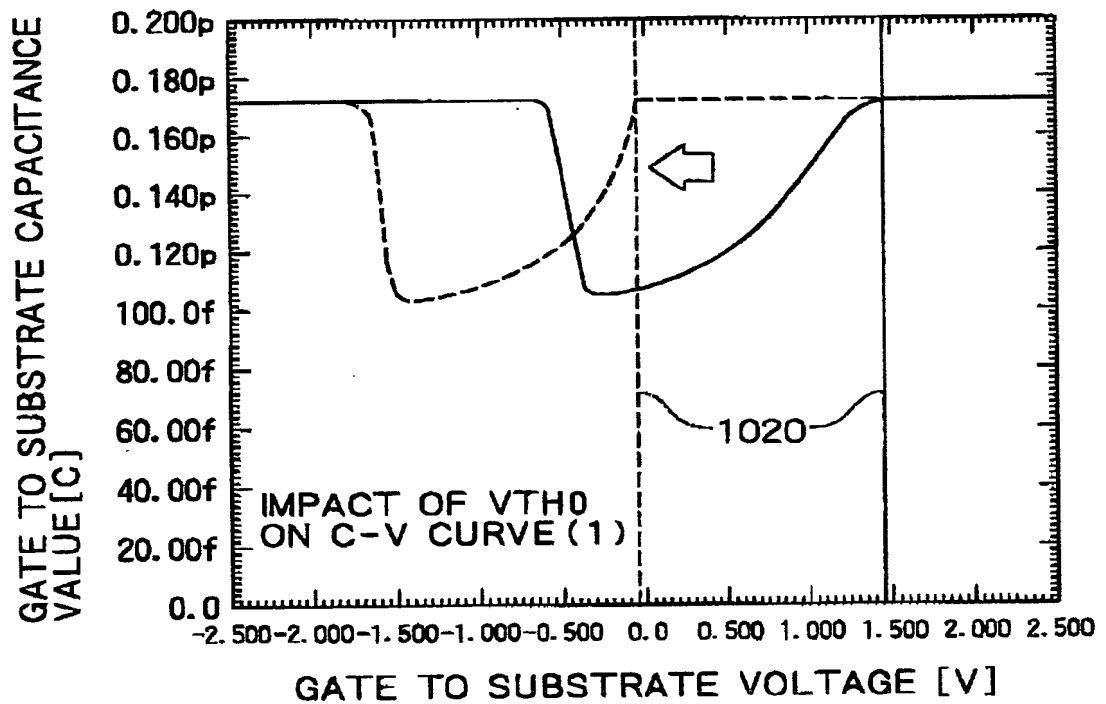


FIG. 13

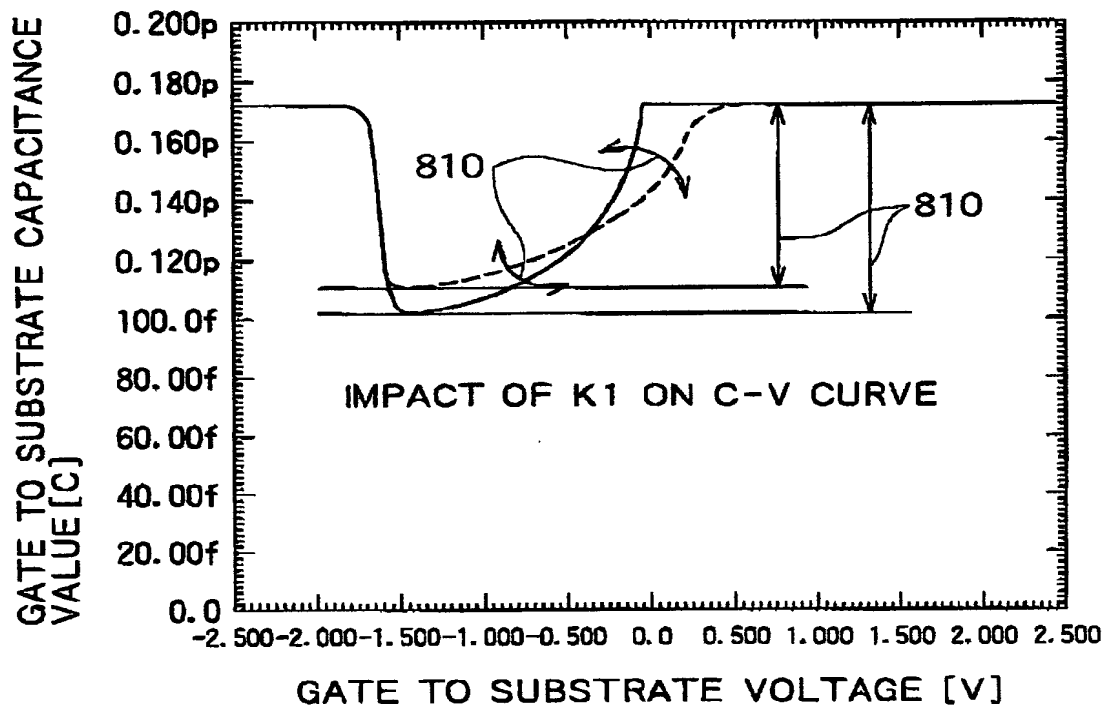


FIG. 14

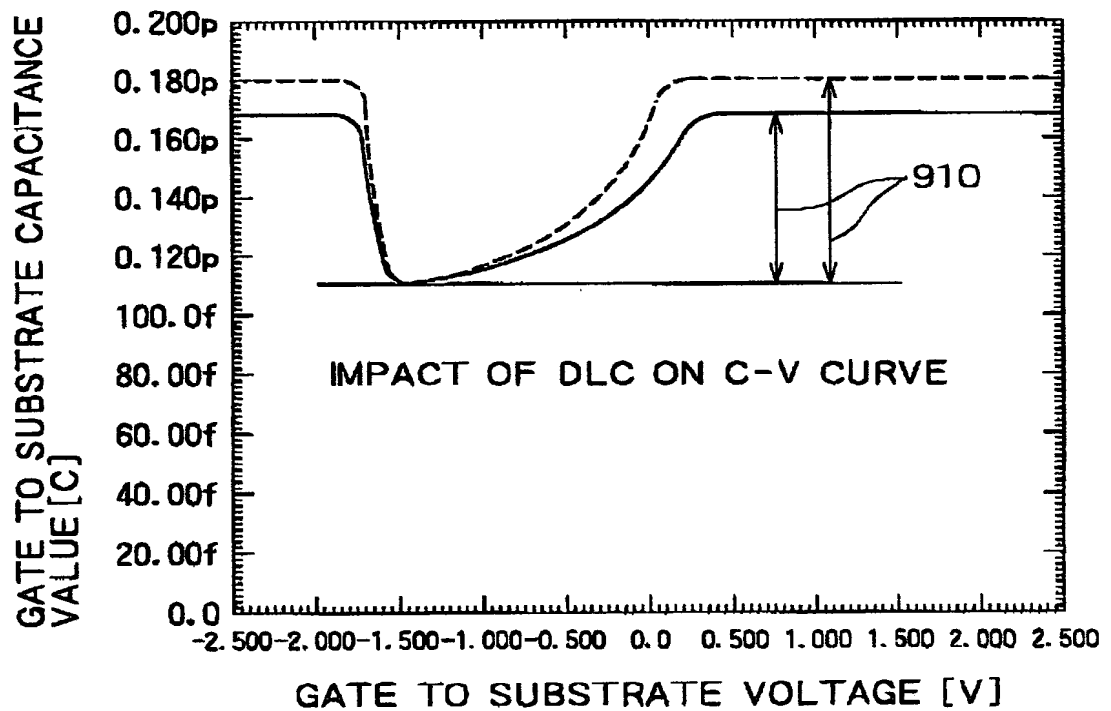


FIG. 15

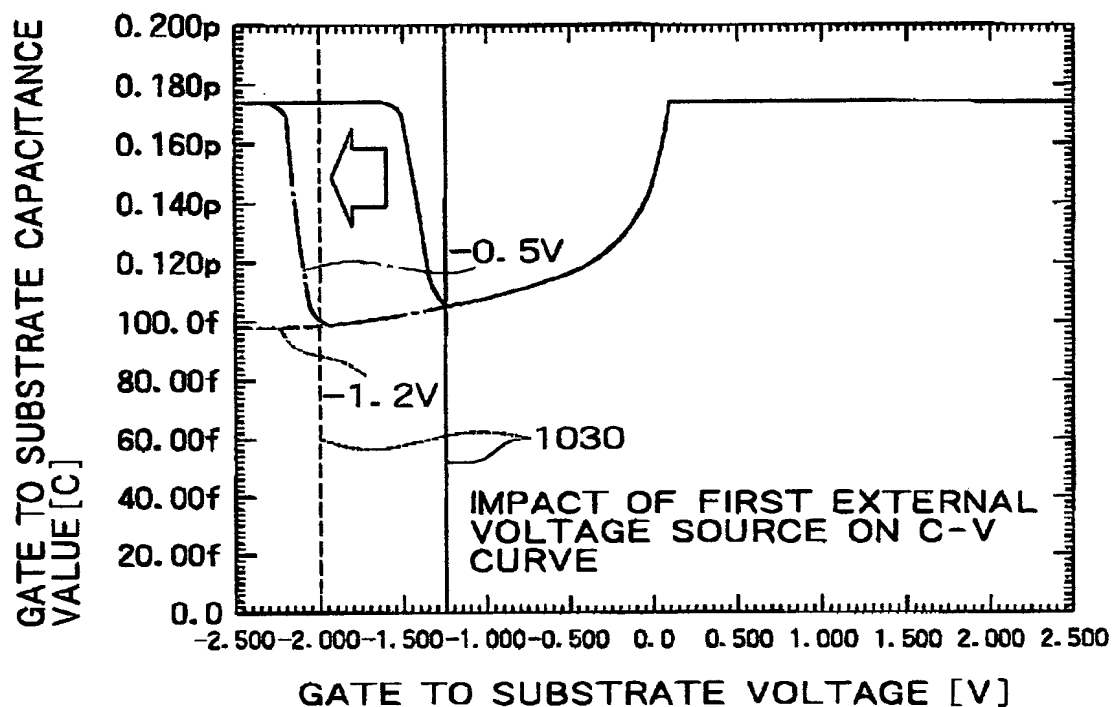


FIG. 16

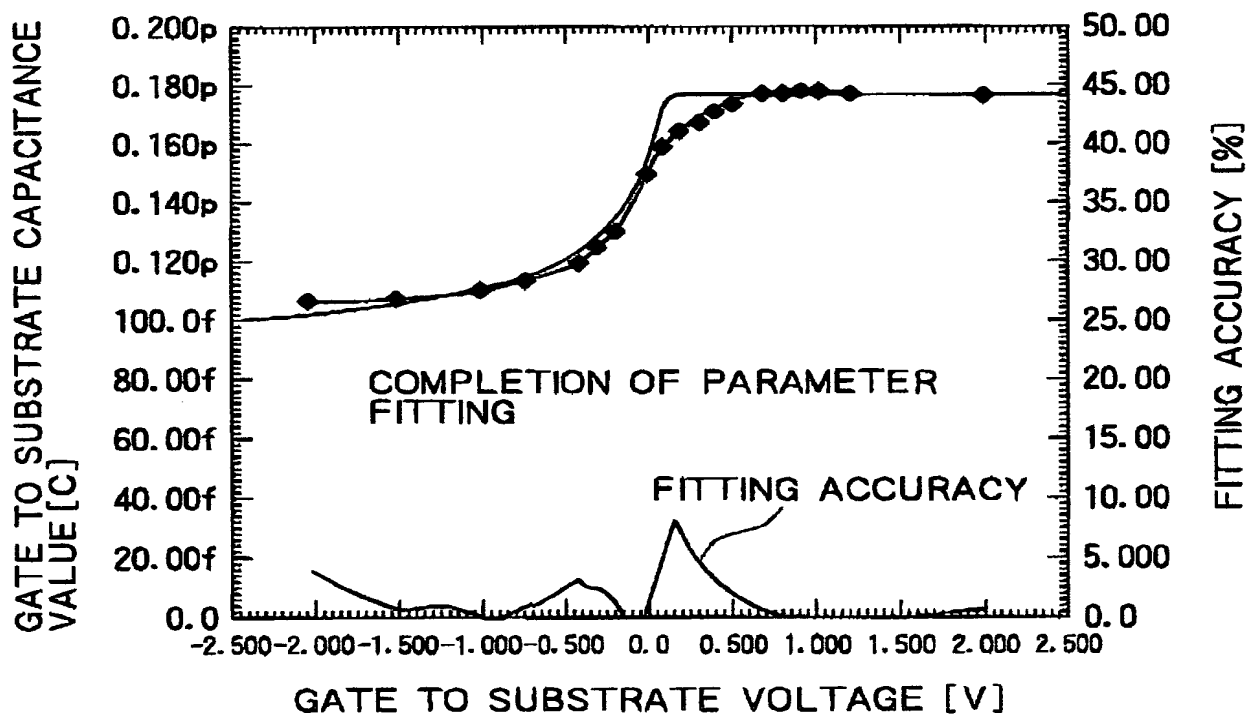


FIG. 17

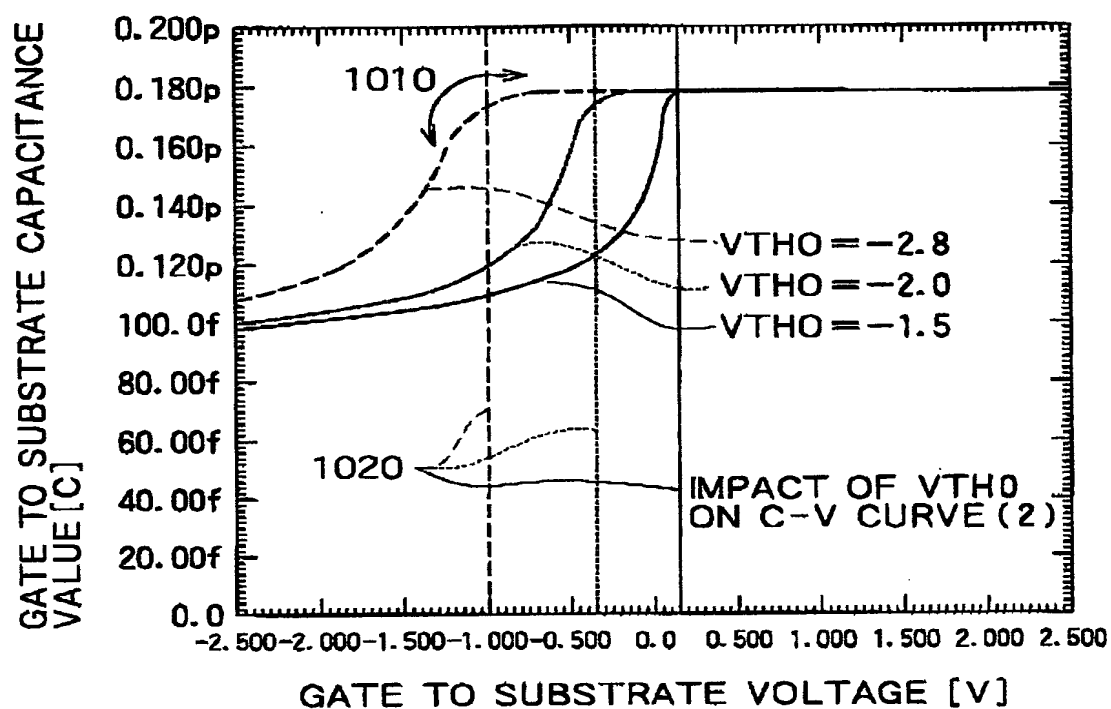


FIG. 18

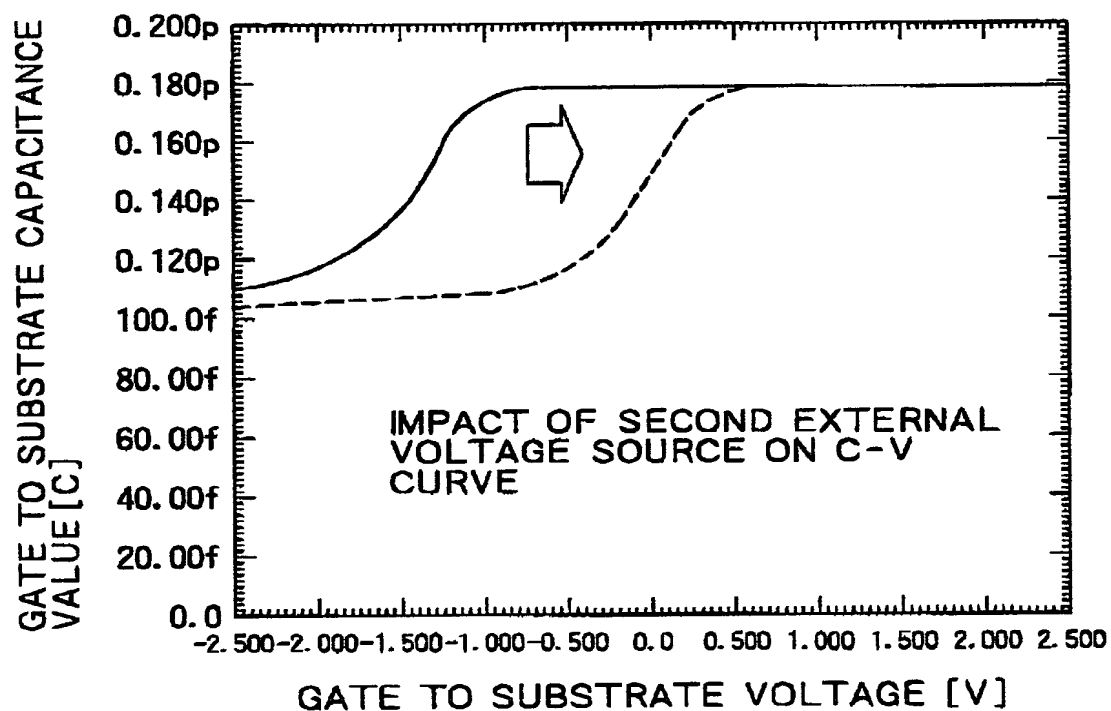


FIG. 19

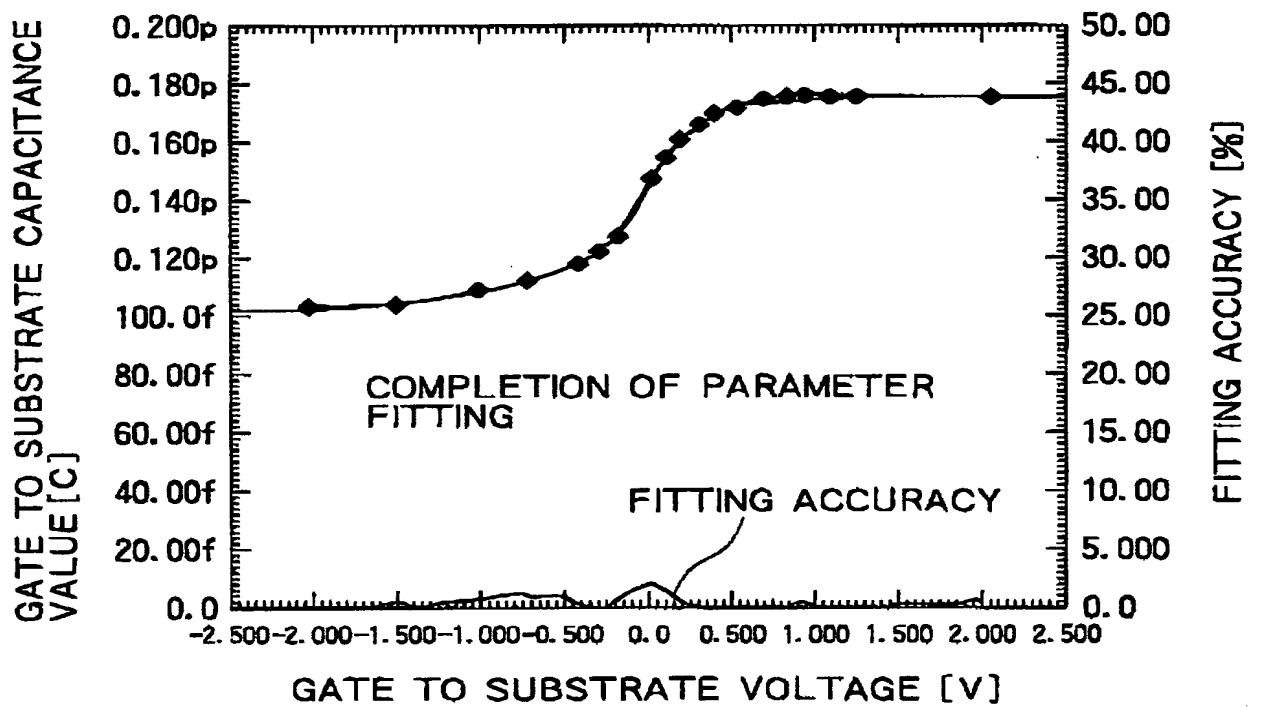


FIG. 20

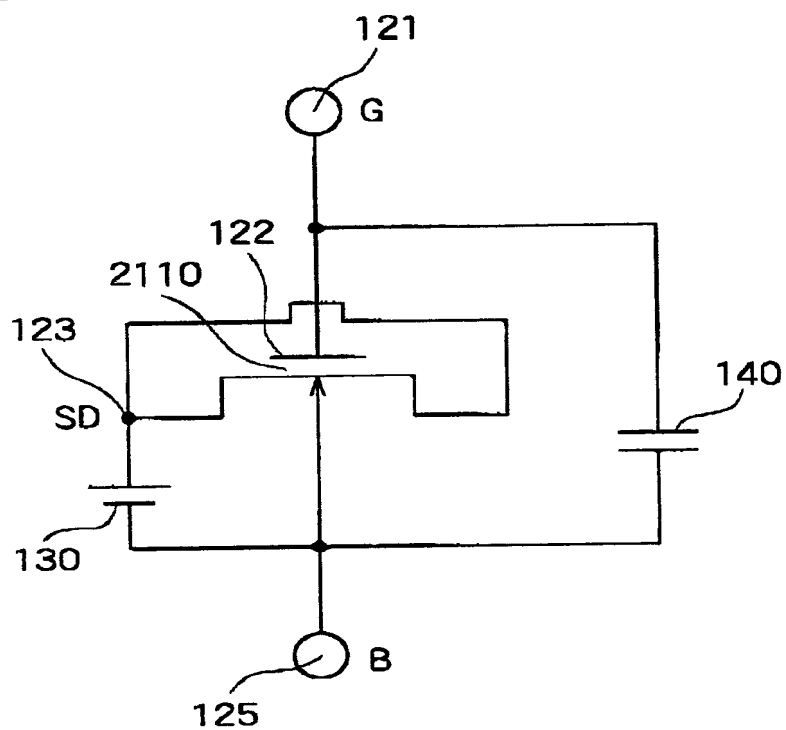


FIG. 21

